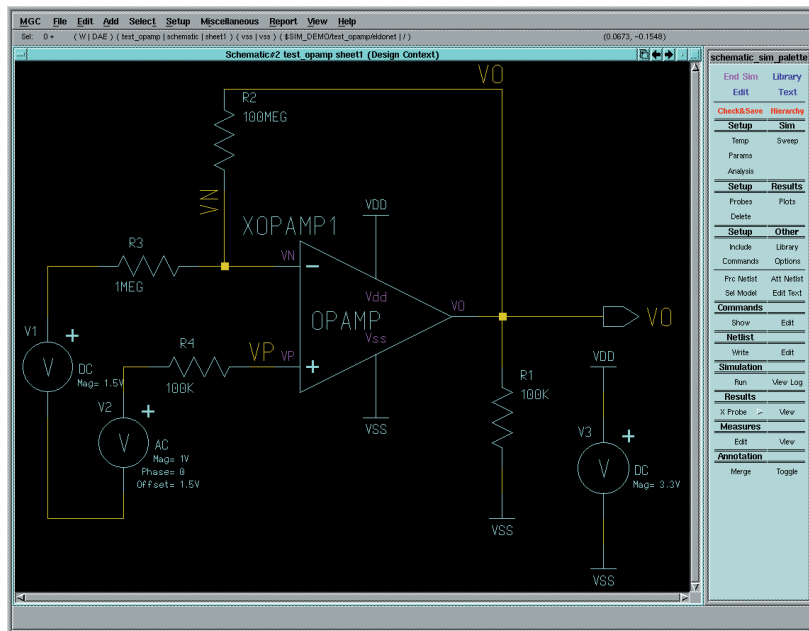


Design Architect-IC



With Design Architect-IC, digital, analog and mixed-signal blocks can be created using schematic and symbol editors, and the model registrar.

A Total Design Solution

Integrated circuit design is becoming more complex every day. This is especially true in analog and mixed-signal design. To address critical time-to-market issues as designs become increasingly complex, Mentor Graphics has developed the IC Station® Tool Suite, a complete IC design flow, from schematic capture to physical layout and verification. With the help of foundry partners, we deliver complete design kit support for your target process technology, ensuring immediate access to essential data. And the IC Station Tool Suite supports multiple hardware platforms so you can make computing environment choices based on your unique cost/performance needs. That's why analog/mixed-signal IC designers who need a total design solution choose the Mentor Graphics IC Station Tool Suite.

Mentor Graphics Design Architect-IC gets your organization productive. Fast.

www.mentor.com/cicd

Major product benefits:

- Shortens design cycles by increasing productivity in schematic capture, simulation setup and analysis for analog/mixed-signal (A/MS) and radio frequency (RF) designs
- Leads to faster and more accurate mixed-signal IC verification
- Reduces hardware costs, leveraging cost-effective Linux workstations
- Enables higher schematic entry productivity, especially for designs with repeated circuitry

Key product features:

- Provides a simulation cockpit for fast setup of simulations of A/MS and RF circuits under a variety of conditions
- Enables easy simulation setup for complex mixed-signal designs
- Models blocks using schematic representations, VHDL, Verilog, SPICE, or VHDL-AMS, and enables simulation of the entire design
- Operates on Linux OS with full UNIX data compatibility
- Offers advanced schematic data modeling techniques, as well as an integrated simulation cockpit and revision control system

Full Custom Analog/Mixed Signal and Radio Frequency IC Design

Design Architect-IC is a powerful tool for capturing and netlisting design data integrated within a full custom IC design flow. Design Architect-IC works with the Mentor Graphics IC Station design tools to conceptualize, develop, simulate, verify, and produce even the most challenging full custom analog and mixed-signal IC designs quickly and accurately—the first time. The designer enjoys a consistent look and feel in one environment, whether creating schematics, block diagrams, symbols or HDL representations.

Schematic Capture, Simulation Setup and Analysis

Design Architect-IC shortens design cycles by creating a high productivity environment for schematic capture and simulation setup for A/MS and RF simulation and analysis. The ability to

simulate analog components quickly and accurately is achieved through an interface with Mentor Graphics Eldo™ and Eldo RF, ADVance™ MS and Mach TA™ products, creating a tight loop for capture, simulation and analysis of designs.

Verification and Simulation

Design Architect-IC interfaces seamlessly with Mentor Graphics simulation tools, enabling quick and accurate simulations of high transistor count processors, memory modules and mixed-signal designs. The result is shorter design cycles due to increased productivity.

Superior Modeling Capability

Design Architect-IC models digital, analog and mixed-signal blocks in a common environment using schematic representations C, VHDL, Verilog, SPICE, VHDL-AMS, or Verilog-AMS.

Then it quickly enables simulation of the entire hierarchical design. Its ability to intelligently make trade-offs in design representations leads to faster and more accurate mixed-signal IC chip verification.

Linux Compatibility

Design Architect-IC leverages the fast processing speeds and lower infrastructure costs of Linux platforms. Linux compatibility gives design engineers the convenience of performing schematic entry and simulation on a portable platform. Using Design Architect-IC, designers can enter and simulate their designs on a PC or run simulations on their UNIX server, realizing significantly lower infrastructure costs due to cost-effective Linux environments.

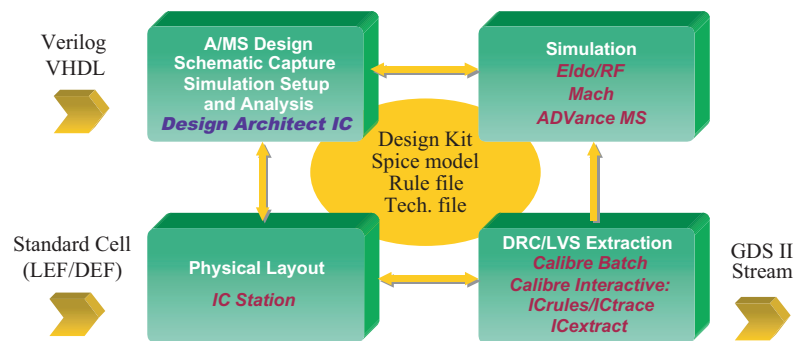
Advanced Modeling Techniques

For power users, Design Architect-IC has advanced schematic modeling techniques that help designers work faster and decrease duplication of effort. Some of the advanced features include:

- Function Blocks
- FOR and CASE frames
- Parameter passing from any point on the hierarchy
- Multiple design versions based on the same set of schematics

With these advanced techniques, designers experience higher schematic entry productivity, especially in designs with repeated circuitry.

Mentor Graphics Analog/Mixed-Signal IC Design Flow



The Design Architect®-IC product is a powerful design capture and netlisting tool that is the front end of the Mentor Graphics complete full custom IC design flow.

Visit our website at www.mentor.com/cicd for the latest product news.

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