

ICX Pro Explorer - Signal Integrity Analysis for high-speed design.

Major product features:

- Complete what-if analysis environment
- Easy to use electrical view of the circuit
- Explore design options early (component selection, topology, termination, stackup, etc.)
- Sweep design parameters to determine operating range of the circuit
- QualityIBIS models provide accurate IBIS models for use right from the start
- Powerful ICX simulation technology provides flexible model support, including:
 - IBIS 4.1
 - SPICE
 - VHDL-AMS
 - S-Parameters

Learning and Exploration

ICX Pro™ Explorer provides an intuitive user interface for engineers to explore signal integrity (SI) solutions in their high-speed designs. Engineers learning SI are offered a concise view of how things work, while those more seasoned are able to investigate in great detail the SI effects in their designs. Components are modeled using industry standard IBIS models, while simulations are provided by our proven ICX® simulation technology. With a library of default IBIS models provided, engineers can begin evaluating high-speed design solutions easily and quickly.

Trace Model Schematics

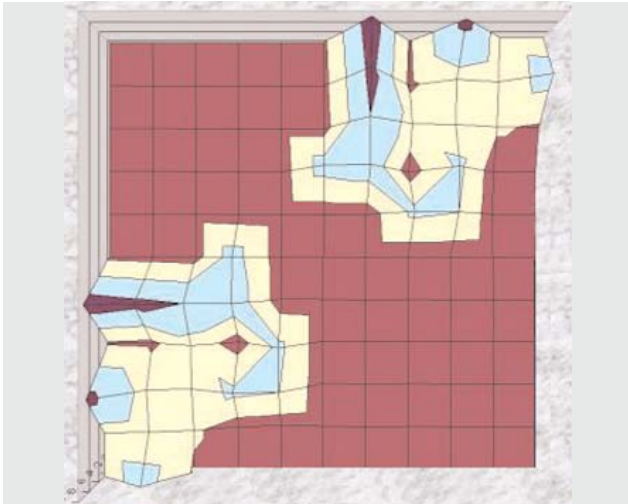
An easy to use electrical view of the circuit allows engineers to more readily understand how the design will behave. This simple view is called a Trace Model Schematic (TMS). Schematics can easily be created manually or automatically from design data (netlist or layout).

Complete What-If Analysis Environment

ICX Pro Explorer enables pre-schematic what-if analysis by exploring and testing various design options. Vary topology, termination, IBIS model selection, board stackup, characteristic impedance and many more design parameters. Simulations with these varying parameters are called sweeps and may be used to determine the optimum design implementation. Design sensitivity to manufacturing tolerances and device variances can be fully explored.

Solution Space Exploration (SSE)

Performing simulation sweeps enables engineers to evaluate circuit performance over a range of operating conditions. SSE delivers that capability in an easy to use three dimensional visual form that clearly shows under which conditions acceptable circuit performance is achieved. These conditions can then be expressed as design constraints.



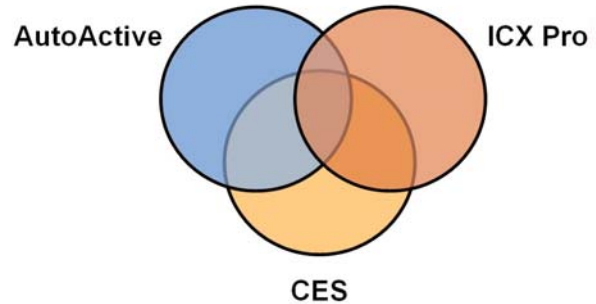
Solution Space Exploration provides a 3-D view in Microsoft Excel (which may be rotated, panned and zoomed) of simulation sweeps for the selection of appropriate design constraints.

Constraint Definition

The Constraint Template Editor built into ICX Pro Explorer provides the same look and feel as CES™ (Constraint Editor System), while enabling designers to enter constraints, perform simulations and validate system performance. Once developed, constraints may be pushed into CES for use in the design flow or as generic constraint templates for use in other designs.

Powerful Simulation Technology

ICX Pro Explorer uses the same powerful simulation technology as ICX. This proven technology supports IBIS v4.1, including EBDs, SPICE, VHDL-AMS and S-Parameter based models. With ICX simulation technology, designers can use the right model the first time.



Integrated high-speed design environment.

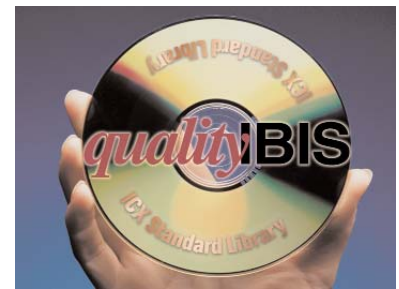
Integrated into a CES Design Flow

Used standalone, or integrated into a CES design flow, ICX Pro Explorer provides SI exploration for constraint definition. The intellectual property that are your design constraints may be re-used design after design.

AutoActive®, ICX Pro Explorer and CES work together to deliver an integrated design solution for improved usability and productivity.

Quality IBIS

With over 13,500 IBIS models, many created from actual measured devices, the QualityIBIS™ library provides users with a comprehensive library of accurate IBIS models.



QualityIBIS models ship for use with ICX Pro Explorer.

These models are ready to use.

Supported Operating Systems:

- HP-UX: 11.0, 11i
- Red Hat Linux 7.3
- Solaris: 8, 9
- Windows: 2000/XP

For more information, visit our website at www.mentor.com/highspeed

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